

SEMESTER I									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Induction Programme</i>									
1	U23MTA01	Induction Programme	MT	0	0	0	0	0	3,4,5,10,16
<i>Theory</i>									
2	U23MAT01	Linear Algebra and Calculus	BS	3	1	0	4	4	4,9
3	U23EGT01	Communicative English	HS	3	0	0	3	3	4
4	U23PYT01	Engineering Physics	BS	3	0	0	3	3	4,7,9
5	U23CST01	Problem Solving and Programming in C	ES	3	0	0	3	3	4,9
6	U23TAT01	Heritage of Tamils	HS	1	0	0	1	1	4,11
<i>Practical's</i>									
7	U23PYP01	Physics Laboratory	BS	0	0	2	1	2	4,9
8	U23CSP01	Problem Solving and Programming in C Laboratory	ES	0	0	2	1	2	4,9
9	U23MEP02	Engineering Drawing	ES	0	0	4	2	4	4,9
Total				13	1	8	18	22	

SEMESTER II									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Theory</i>									
1	U23MAT02	Transforms and Boundary value problems	BS	3	1	0	4	3	4,9
2	U23CYT01	Engineering Chemistry	BS	3	0	0	3	4	6,7,12
3	U23CST02	Python Programming	ES	3	0	0	3	3	4,9
4	U23ECT01	Electric Circuit and Electron Devices	PC	3	0	0	3	3	7,9
5	U23EGT02	Professional English	HS	3	0	0	3	3	4,8
6	U23TAT02	Tamils and Technology	HS	1	0	0	1	1	4,9,11
<i>Practical's</i>									
7	U23CYP01	Chemistry Laboratory	BS	0	0	2	1	2	6,12
8	U23CSP02	Python Programming Laboratory	ES	0	0	2	1	2	4,9
9	U23ECP04	Electric Circuit and Electron Devices Laboratory	PC	0	0	2	1	2	7,9
Total				16	1	6	20	23	

SEMESTER III									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Theory</i>									
1	U23MAT06	Probability and Random Process	BS	3	1	0	4	4	4&9
2	U23CST03	Data Structures	ES	3	0	0	3	3	4&9
3	U23EVT31	Digital System Design	PC	3	0	0	3	3	9
4	U23EVT01	Analog and Digital Communication	PC	3	0	0	3	3	9,11
5	U23EVT02	Electronic Devices and Circuits	PC	3	0	0	3	3	7,9
<i>Practical's</i>									
6	U23CSP03	Data Structures Laboratory	ES	0	0	2	2	4	4,9
7	U23EVP31	Digital System Design Laboratory	PC	0	0	4	2	4	9
8	U23EVP01	Analog and Digital Communication Lab	PC	0	0	2	2	4	9,11
9	U23EVP02	Electronic Devices and Circuits Laboratory	PC	0	0	2	2	4	7,9
10	U23EVI01	Core Course Project I	EEC	0	0	2	1	2	4,9
Total				15	1	12	25	34	

SEMESTER IV									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Theory</i>									
1	U23ECT10	Linear Integrated Circuits	PC	3	0	0	3	3	7,9
2	U23EVT03	Electro Magnetic Fields and Transmission lines	PC	3	0	0	3	3	7,9
3	U23EVT04	SystemVerilog	PC	3	0	0	3	3	4,9
4	U23EVT05	Analog Integrated Circuits Design	PC	3	0	0	3	3	7,9
5	U23EVT06	CMOS Integrated Circuits	PC	3	0	0	3	3	7,9,12
6	U23XXXXX	Mandatory Course I	MT	2	0	0	0	2	***
<i>Practical's</i>									
7	U23ECP10	Linear Integrated Circuits Laboratory	PC	0	0	4	2	4	7,9
8	U23EVP03	Hardware modelling Using SystemVerilog Laboratory	PC	0	0	4	2	4	4,9
9	U23EVP04	CMOS IC Laboratory	PC	0	0	4	2	4	7,9,12
10	U23EVI02	Core Course Project II	EEC	0	0	4	1	2	4,9
Total				17	0	16	22	31	

SEMESTER V									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Theory</i>									
1	U23EVT07	Digital Signal Processing Algorithms	PC	3	0	0	3	3	9
2	U23EVT08	Computer Architecture and On-Chip Communication	PC	3	0	3	3	3	9
3	U23EVT09	Semiconductor Manufacturing Technology	PC	3	0	0	3	3	7,9,12
4	U23EVT10	SoC Design I: RTL Design and Verification	PC	3	0	0	3	3	9
5	U23EXXXX	Professional Elective - I	PE	3	0	0	3	3	*
6	U23XXXX	Open Elective - I	OE	3	0	0	3	3	**
7	U23XXXXX	Mandatory Course II	MT	2	0	0	0	2	***
<i>Practical's</i>									
8	U23EVP05	Digital Signal Processing Laboratory	PC	0	0	2	2	4	9
9	U23EVP06	RTL Design and Verification Laboratory	PC	0	0	2	2	4	9
10	U23EVI03	Core Course Project III	EEC	0	0	4	1	2	4,9
Total				20	0	11	22	30	

SEMESTER VI									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Theory</i>									
1	U23EVT11	Embedded Systems Architecture and Design	PC	3	0	0	3	3	7,9,11
2	U23EVT12	VLSI Testing and Validation	PC	3	0	0	3	3	9,12
3	U23EVT13	SoC Design II: Physical Design and Chip Implementation	PC	3	0	0	3	3	7,9,12
4	U23EVT14	Wireless Communication Systems	PC	3	0	0	3	3	9,11
5	U23EXXXX	Professional Elective - II	PE	3	0	0	3	3	*
6	U23XXXX	Open Elective - II	OE	3	0	0	3	3	**
<i>Practical's</i>									
7	U23EVP07	Embedded Systems Laboratory	PC	0	0	2	2	4	9,11
8	U23EVP08	Physical Design Laboratory	PC	0	0	2	2	4	7,9,12
9	U23EVI04	Core Course Project IV	EEC	0	0	4	1	2	4,9
Total				18	0	8	23	28	

SEMESTER VII									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
<i>Theory</i>									
1	U23EVT15	Electronic Packaging and PCB Design	PC	3	0	0	3	3	9,12
2	U23EXXXX	Professional Elective - III	PE	3	0	0	3	3	*
3	U23EXXXX	Professional Elective - IV	PE	3	0	0	3	3	*
4	U23EXXXX	Professional Elective - V	PE	3	0	0	3	3	*
5	U23EXXXX	Professional Elective - VI	PE	3	0	0	3	3	*
<i>Practical's</i>									
6	U23EVP09	Electronic Packaging and PCB Prototyping Laboratory	PC	0	0	2	2	4	9,12
7	U23EVJ01	Project Work Phase I	EEC	0	0	12	6	12	
Total				12	0	14	23	31	

SEMESTER VIII									
S.No	Course Code	Course Title	CC	L	T	P	C	CP	SDG
1	U23EVJ02	Project Work Phase II / Internship	EEC	0	0	12	12	12	
TOTAL CREDITS						167			