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Department of Electronics Engineering

VISION

To develop competent engineering professionals with strong knowledge and practical skills in VLSI and emerging technologies, fostering research, innovation, and ethical values to meet the needs of industry and society.

Courses Offered Undergraduate

B.E. Electronics Engineering
(VLSI Design and Technology)

Duration: 4 Years

Postgraduate

M.E. VLSI Design and Embedded Systems
Duration: 2 Years

● Mission

MS1 Technical Knowledge and Innovation To provide quality education in VLSI and emerging technologies through effective teaching, continuous learning, research, and innovation.

MS2 Practical Skills and Industry Exposure To develop practical and professional skills through laboratory learning, projects, internships, industrial training, and interaction with industry.

MS3 Professional and Ethical Development To develop communication, leadership, entrepreneurial, and lifelong learning skills while promoting professional ethics and social responsibility.



Why VLSI at CIET?

A semiconductor-focused engineering programme that develops students from digital logic and electronics fundamentals through RTL design, verification, SoC architecture, physical design, semiconductor manufacturing, packaging, testing and emerging chip technologies.

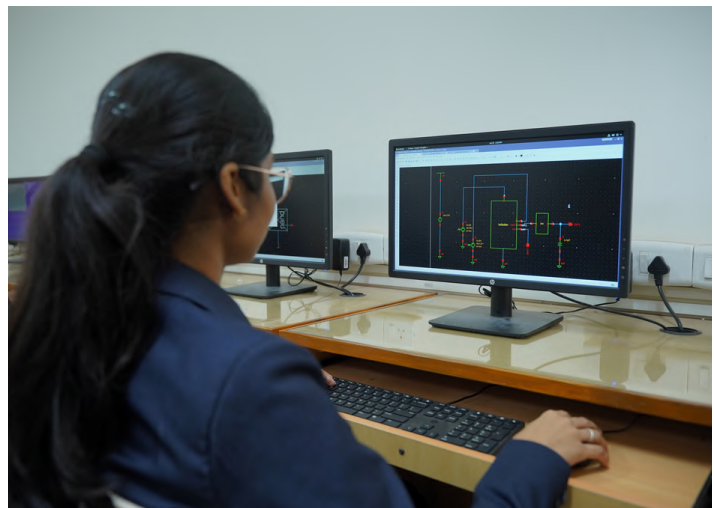


"We are CIETians"



Testimonials

"The VLSI programme at CIET gives us valuable hands-on experience with industry-standard EDA tools and real-time chip-design projects. The C2S initiative provides an excellent platform to learn and explore the semiconductor field."



"India is moving from being a consumer of semiconductor technology to a designer and developer of chips, and CIET aims to prepare CIETians to be part of that transformation."

Chips to Startup (C2S) at CIET

CIET is part of the MeitY Chips to Startup (C2S) Programme, supporting India's growing semiconductor industry. Through C2S, CIETians get hands-on experience in VLSI, chip design, EDA tools, SoC design, and semiconductor technologies. It helps students turn their ideas into real chip-design projects and prepares them for higher studies, industry, research, and semiconductor startups. Learn VLSI. Design Chips. Build the Future.



Chips to Startup lab



Testimonials

"The VLSI laboratory and computing facilities at CIET provide a great environment for learning chip design, verification and SoC technologies. The practical exposure helps us build confidence for careers in the semiconductor industry."



● Computing Facility

Server – HPE ProLiant DL360 Gen10, Rack Server

- Processor: 2 × Intel Xeon Gold 6138, 20 Cores each
- Total CPU Cores: 40 cores
- Memory: 512 GB RAM, 2400 MHz
- Storage: 2 × 480 GB SATA SSD
- Additional Storage: 2 × 1.92 TB SATA SSD
- RAID: Dedicated RAID Controller
- Power: Dual Redundant Power Supply (RPS)

Client Systems – 35 Nos. HP 280

- Processor: Intel Core i7, 13th Generation
- RAM: 16 GB
- Storage: 512 GB NVMe SSD
- Display: 22-inch LED Monitor